What is claimed is:

1. A method comprising:

receiving data in a receive buffer; and

sending a hold command to a transmitting node currently sending data to hold transmission of additional data when a level of said data in said receive buffer reaches an adjustable high threshold level.

2. The method of claim 1, wherein said adjustable high threshold is adjustable in response to a transmission rate of said additional data.

3. The method of claim 1 further comprising:

receiving a hold acknowledge command acknowledging said hold command, and wherein said adjustable high threshold level is adjustable in response to an elapsed time interval between sending of said hold command and receiving of said hold acknowledge command.

4. The method of claim 1, further comprising:

holding transmission of said additional data until said data in said receive buffer reaches a low threshold level.

5. The method of claim 4, wherein said low threshold level comprises an adjustable low threshold level.

6. An apparatus comprising:

circuitry capable of receiving data in a receive buffer, and sending a hold command to a transmitting node currently sending data to hold transmission of additional data when a level of said data in said receive buffer reaches an adjustable high threshold level.

7. The apparatus of claim 6, wherein said adjustable high threshold is adjustable in response to a transmission rate of said additional data.

8. The apparatus of claim 6, wherein said circuitry is further capable of receiving a hold acknowledge command acknowledging said hold command, and wherein said adjustable high threshold level is adjustable in response to an elapsed time interval between sending of said hold command and receiving of said hold acknowledge command.

9. The apparatus of claim 6, wherein said circuitry is further capable of holding transmission of said additional data until said data in said receive buffer reaches a low threshold level.

10. The apparatus of claim 9, wherein said low threshold level comprises an adjustable low threshold level.

11. An article comprising:

circuitry comprising a receive buffer to receive data, said receive buffer having a high threshold level, and said circuitry capable of sending a hold command to a transmitting node

sending data to hold transmission of additional data when a level of said data in said receive buffer reaches said high threshold level; and

a storage medium having stored therein instructions that when executed by a machine results in the following:

adjusting said high threshold level.

12. The article of claim 11, wherein said storage medium having stored therein instructions that when executed by said machine also results in the following:

adjusting said high threshold level in response to a transmission rate of said additional data.

13. The article of claim 11, wherein said storage medium having stored therein instructions that when executed by said machine also results in the following:

adjusting said high threshold level in response to an elapsed time interval from said sending of said hold command to receipt of a hold acknowledge command from said transmitting node.

14. The article of claim 11, wherein said circuitry is further capable of maintaining said command to hold transmission of said additional data until said data in said receive buffer reaches a low threshold level.

15. The article of claim 11, wherein said storage medium having stored therein instructions that when executed by said machine also results in the following:

adjusting said low threshold level.

16. The article of claim 15, wherein said storage medium having stored therein instructions that when executed by said machine also results in the following:

adjusting said low threshold level in response to a transmission rate of said additional data.

17. A system comprising:

a circuit card comprising an integrated circuit, said integrated circuit comprising circuitry capable of receiving data in a receive buffer, and sending a hold command to a transmitting node currently sending data to hold transmission of additional data when a level of said data in said receive buffer reaches an adjustable high threshold level.

18. The system of claim 17, wherein said adjustable high threshold is adjustable in response to a transmission rate of said additional data.

19. The system of claim 17, wherein said circuitry is further capable of receiving a hold acknowledge command acknowledging said hold command, and wherein said adjustable high threshold level is adjustable in response to an elapsed time interval between sending of said hold command and receiving of said hold acknowledge command.

20. The system of claim 17, wherein said circuitry is further capable of holding transmission of said additional data until said data in said receive buffer reaches a low threshold level.

21. The system of claim 20, wherein said low threshold level comprises an adjustable low threshold level.